



HTCC-AB02

LoRa Development Board



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Documents

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P 1/14

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Heltec Automation © Limited standard files



Document version

Version	Time	Description
V1.0	2020-04-30	Documents creating
V1.1	2021-04-01	Document structure update
V1.2	2024-01-17	Pin map diagram correction

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1. Description

1.1 Overview

[CubeCell](#) (TM) is a new product series made by Heltec team, mainly for LoRa/LoRaWAN node applications.

CubeCell (TM) series is based on ASR605x (ASR6501, ASR6502), those chips are already integrated with the PSoC® 4000 series MCU (ARM® Cortex® M0+ Core) and SX1262. We have done a lot of migration and development, made it perfectly support [Arduino](#)®, can run the LoRaWAN protocol stably, can easily connect lithium batteries and solar panels.

HTCC-AB02 is a Dev-Board. Friendly designed for developers, easy to verify communication solutions.

HTCC-AB02 are available in two product variants:

Table 1.1 Product model list

No.	Model	Description
1	HTCC-AB02-L	470~510MHz working LoRa frequency, used for China mainland (CN470) LPW band.
2	HTCC-AB02-F	For EU868, IN865, US915, AU915, AS923, KR920 and other LPW networks with operating frequencies between 863~928MHz.

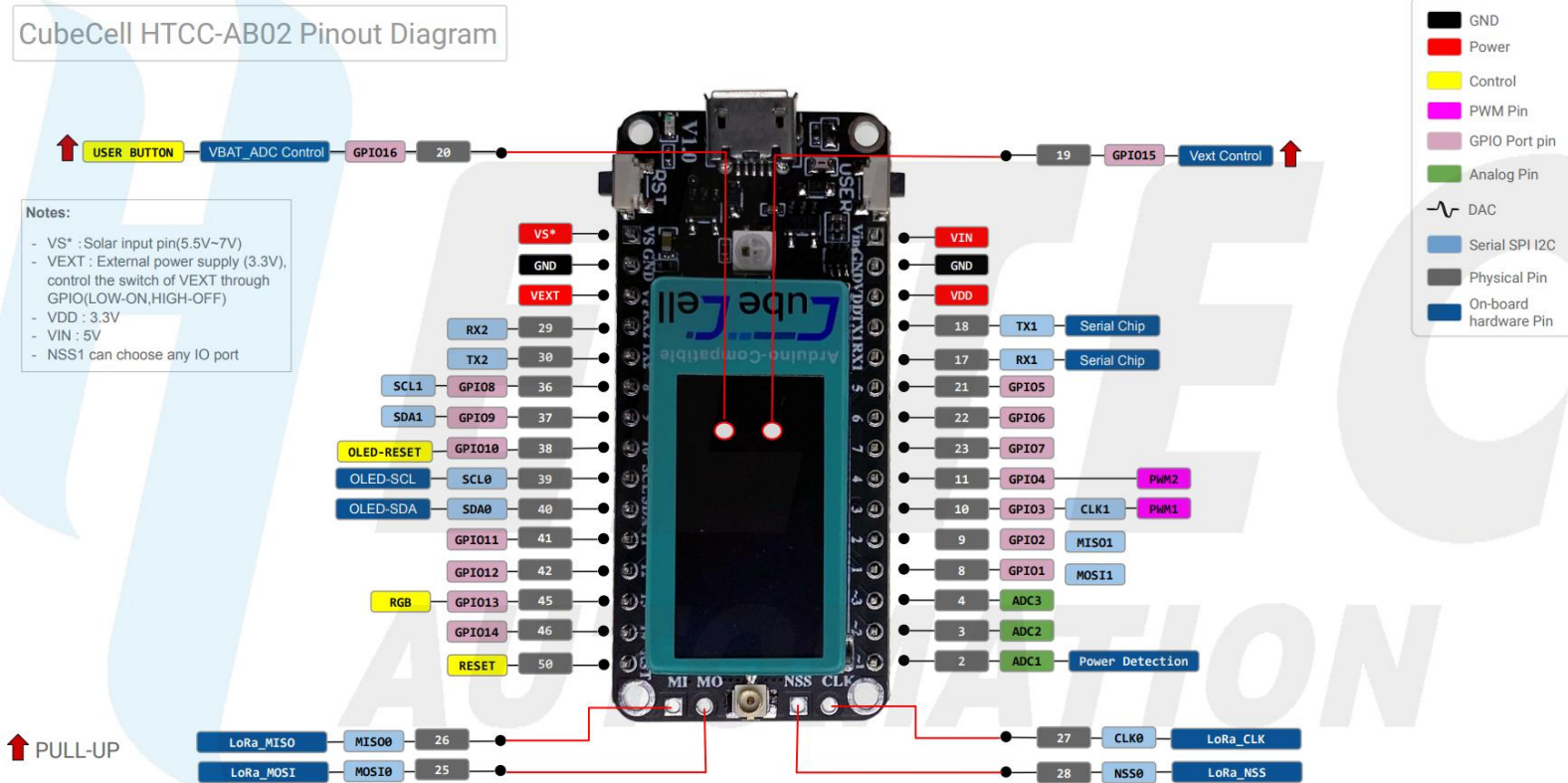


1.2 Product features

- Perfect [Arduino-Compatible!](#)
- Based on ASR605x (ASR6501, ASR6502), those chips are already integrated the PSoC® 4000 series MCU (ARM® Cortex® M0+ Core) and SX1262;
- LoRaWAN 1.0.2 support;
- Ultra low power design, 3.5uA in deep sleep;
- Onboard solar energy management system, can directly connect with a 5.5~7V solar panel;
- Onboard SH1.25-2 battery interface, integrated lithium battery management system (charge and discharge management, overcharge protection, battery power detection, USB / battery power automatic switching);
- Micro USB interface with complete ESD protection, short circuit protection, RF shielding, and other protection measures;
- Integrated CP2102 USB to serial port chip, convenient for program downloading, debugging information printing;
- Good impedance matching and long communication distance.
- Onboard 0.96-inch 128*64 dot matrix OLED display, which can be used to display debugging information, battery power, and other information.

2. Pin Definition

2.1 Pin assignment



2.2 Pin description

● Header P2

Table 2-2-1 Pin description

No.	Name	Type	Function
1	VS	P	Power Supply.
2	GND	P	Ground.
3	VE	P	Vext.
4	RX2	I/O	UART_RX2.
5	TX2	I/O	UART_TX2.
6	8	I/O	GPIO8, SCL1.
7	9	I/O	GPIO9, SDA1.
8	10	I/O	GPIO10,OLED_RESET.

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8	SCL	I/O	OLED_SCL0.
9	SDA	I/O	OLED_SDA0.
10	11	I/O	GPIO11.
11	12	I/O	GPIO12.
12	13	I/O	GPIO13, RGB.
13	14	I/O	GPIO14.
14	RST	I	RESET.

Header P1

Table 2-2-2 Pin description

No.	Name	Type	Function
1	Vin	P	Power Supply.
2	GND	P	Ground.
3	VDD	P	3.3V Power.
4	TX1	I/O	UART_TX1. (Serial chip).
5	RX1	I/O	UART_RX1. (Serial chip).
6	5	I/O	GPIO5.
7	6	I/O	GPIO6.
8	7	I/O	GPIO7.
9	4	I/O	GPIO4, PWM2.
10	3	I/O	GPIO3, PWM1,CLK1.
11	2	I/O	GPIO2, MISO1.
12	1	I/O	GPIO1, MOSI.
13	~3	I/O	ADC_IN3.
14	~2	I/O	ADC_IN2.
15	~1	I/O	ADC_IN1, Power detection.



3. Specifications

3.1 General specifications

Table 3-1: General specifications

Parameters	Description
Master Chip	ASR6502 (48 MHz ARM® Cortex® M0+ MCU)
LoRa Chipset	SX1262
USB to Serial Chip	CP2102
Frequency	470~510 MHz, 863~923 MHz
Max TX Power	22 ± 1 dBm
Receiving sensitivity	-135 dBm
Solar Energy	5.5~7V solar panel
Low Power	Deep Sleep 3.5μA
Hardware Resource	UART x 2; SPI x 1; I2C x 1; SWD x 1; 12-bits ADC input x 3; 8 – channel DMA engine; GPIO x 16
Memory	128KB internal FLASH; 16KB internal SRAM
Interface	Micro USB x 1; LoRa Antenna interface(IPEX 1.0) x 1; 15 x 2.54 pin x 2+2 x 2.54 pin x 3
Battery	3.7V Lithium (SH1.25 x 2 socket)
Operating temperature	-20 ~ 70 °C
Dimensions	51.9 x 25 x 8 m ³
Display Size	0.96-inch OLED



3.2 Power supply

Except when USB or 5V Pin is connected separately, lithium battery can be connected to charge it. In other cases, only a single power supply can be connected.

Table 3-2: Power supply

Power supply mode	Minimum	Typical	Maximum	Company
USB powered($\geq 500\text{mA}$)	4.7	5	6	V
Lithium battery($\geq 250\text{mA}$)	3.3	3.7	4.2	V
5V pin($\geq 500\text{mA}$)	4.7	5	6	V
3V3 pin($\geq 150\text{mA}$)	2.7	3.3	3.5	V

3.3 Power output

Table 3-3: Power output

Output Pin	Minimum	Typical	Maximum	Company
3.3V Pin			500	mA
5V Pin (USB Powered only)		Equal to the input current		
Vext Pin			350	mA

3.4 Power characteristics

Table 3-4: Power characteristics

Mode	Condition	Min.	Typical	Max.	Company
Power	LoRa Rx Mode		10		mA



Consumption(mA)	LoRa 10dB output		70		mA
	LoRa 14dB output		90		mA
	LoRa 17dB output		100		mA
	LoRa 20dB output		105		mA
	Sleep Mode (USB powered)		9.6		mA
	Sleep Mode (VBAT/battery powered)		11		μ A
	Sleep Mode (3.3V header powered)		3.5		μ A

3.5 LoRa RF characteristics

3.5.1 Transmit power

Table3-5-1 Transmit power

Operating frequency band	Maximum power value/[dBm]
470~510	22 $\pm$ 1
867~870	22 $\pm$ 1
902~928	22 $\pm$ 1

3.5.2 Receiving sensitivity

The following table gives typically sensitivity level of the HTCC-AB02-(L/H).

Table3-5-2: Receiving sensitivity

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Signal Bandwidth/[KHz]	Spreading Factor	Sensitivity/[dBm]
125	SF12	-135
125	SF10	-130
125	SF7	-124

3.6 Operation Frequencies

HTCC-AB02(F) supports LoRaWAN frequency channels and models corresponding table.

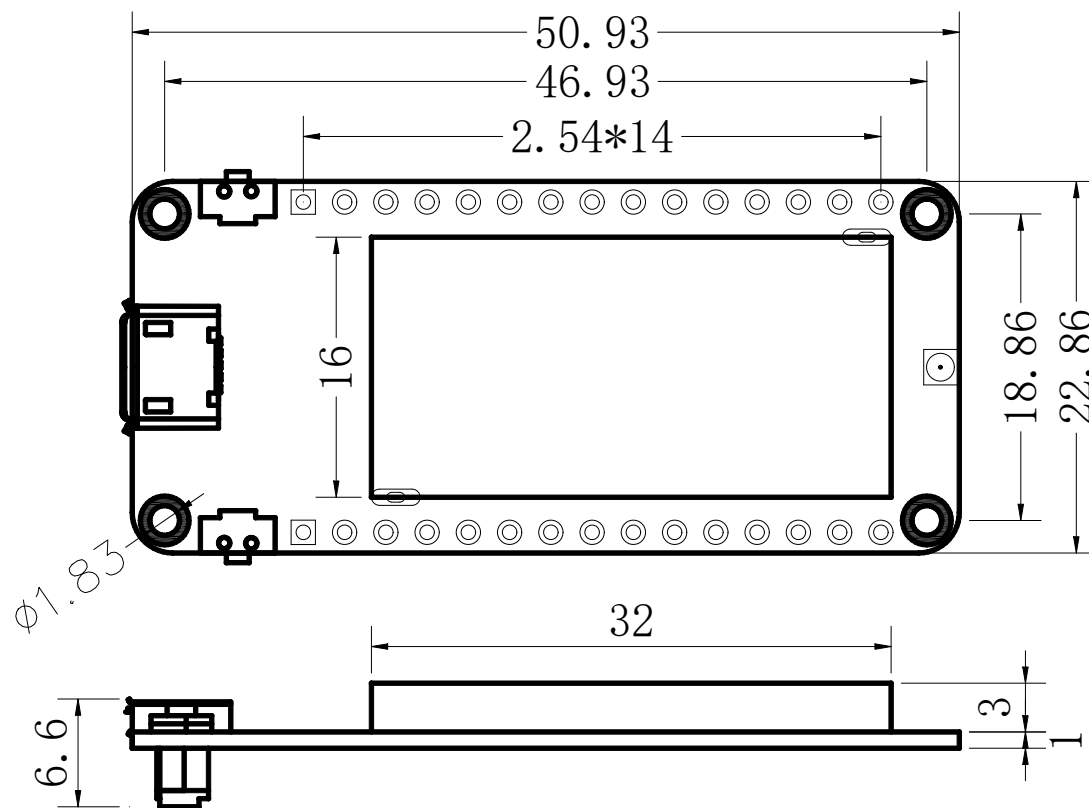
Table3-6: Operation Frequencies

Region	Frequency (MHz)	Model
EU433	433.175~434.665	HTCC-AB02-L
CN470	470~510	HTCC-AB02-L
IN868	865~867	HTCC-AB02-F
EU868	863~870	HTCC-AB02-F
US915	902~928	HTCC-AB02-F
AU915	915~928	HTCC-AB02-F
KR920	920~923	HTCC-AB02-F
AS923	920~925	HTCC-AB02-F



4. Hardware resource

4.1 Physical dimensions





5. Resource

5.1 Relevant Resource

- [Heltec Cubecell Development framework](#)
- [Schematic diagram](#)
- [Pin map](#)
- [Relevant resource](#)

5.2 Contact Information

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